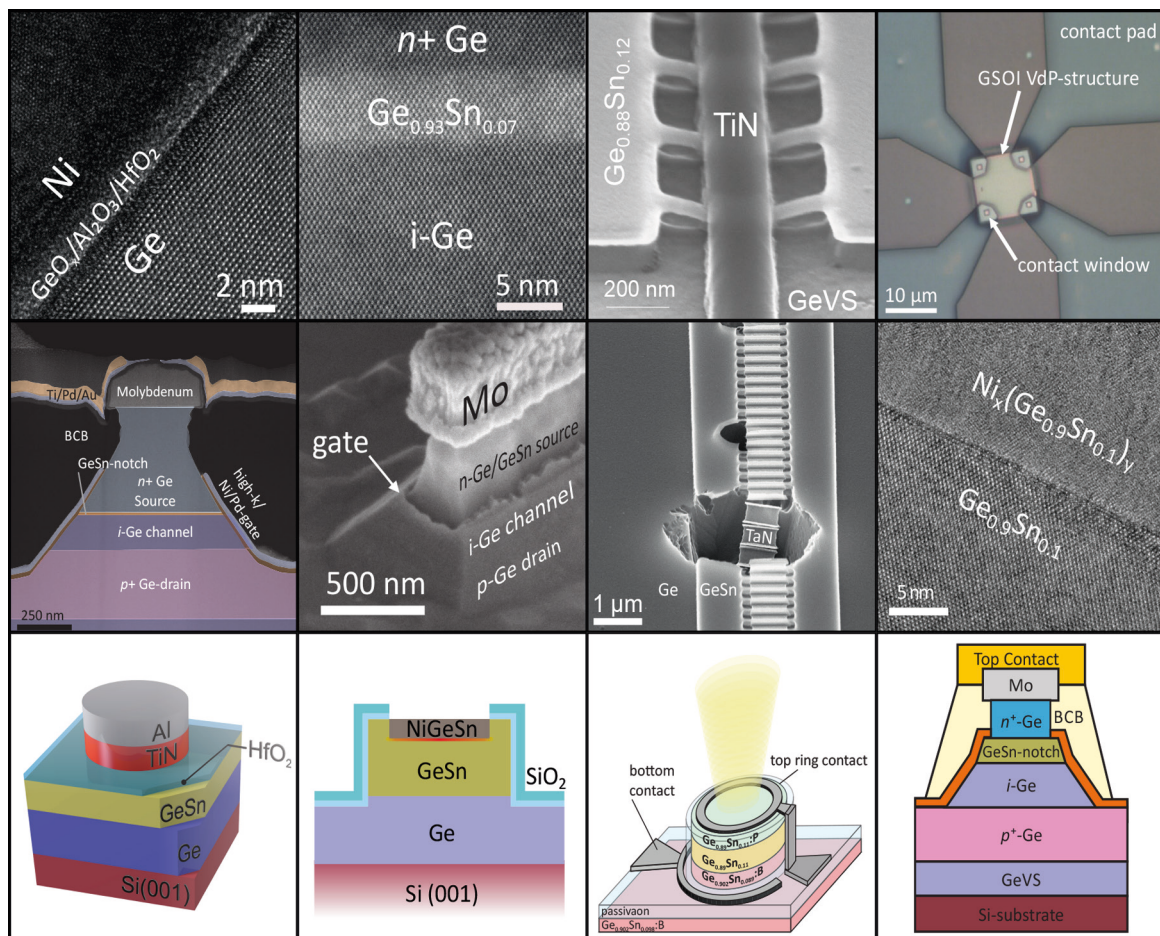




Investigation of GeSn as Novel Group IV Semiconductor for Electronic Applications

Christian Schulte-Braucks

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